

## United States Patent and Trademark Office

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/493,319	01/28/2000	Samson Huang	INTL-0312-US (P7995)	2102
7590 03/02/2004		EXAMINER  JORGENSEN, LELAND R		
Timothy N Trop Trop Pruner Hu & MIles PC 8554 Kathy Freeway Ste 100				
			ART UNIT	PAPER NUMBER
Houston, TX 77024			2675	18
		DATE MAILED: 03/02/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	_		
Advisory Action		09/493,319	HUANG, SAMSON			
		Examiner	Art Unit			
		Leland R. Jorgensen	2675			
	The MAILING DATE of this communication appe	ears on the cover sheet with the c	orrespondence address			
	THE REPLY FILED 10 February 2004 FAILS TO PLACE Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applica ) a timely filed amendment which it (with appeal fee); or (3) a timel	ation. A proper reply to a h places the application in			
	PERIOD FOR RE	EPLY [check either a) or b)]				
	a) The period for reply expiresmonths from the mailin	g date of the final rejection.	in the final rejection, whichever is later. It	_		
	b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	later than SIX MONTHS from the mailin	g date of the final rejection.	''		
	Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Offit timely filed, may reduce any earned patent term adjustment. See 37 C	of extension and the corresponding amo the shortened statutory period for reply ce later than three months after the mai	ount of the fee. The appropriate extension originally set in the final Office action; or	•		
	<ol> <li>A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFI</li> </ol>	s Brief must be filed within the pe R 1.191(d)), to avoid dismissal o	eriod set forth in of the appeal.			
	2. The proposed amendment(s) will not be entered b	ecause:				
(a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);						
l	(b) ☐ they raise the issue of new matter (see Note b	below);				
(c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or						
	(d) they present additional claims without cancel NOTE:	ing a corresponding number of f	inally rejected claims.			
	3. Applicant's reply has overcome the following rejection	etion(s):				
	Newly proposed or amended claim(s) would canceling the non-allowable claim(s).		eparate, timely filed amendment			
	5.⊠ The a) affidavit, b) exhibit, or c) req the application in condition for allowance be 6.☐ The affidavit or exhibit will NOT be considered bed raised by the Examiner in the final rejection.	pecause: See Continuation Sheet.		9		
	7. For purposes of Appeal, the proposed amendmen explanation of how the new or amended claims w					
	The status of the claim(s) is (or will be) as follows:					
	Claim(s) allowed:					
	Claim(s) objected to:					
	Claim(s) rejected: 45 - 54.	•				

STEVEN SARAS

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600

10. Other: \_\_\_\_

Claim(s) withdrawn from consideration: 1 - 44.

8. The drawing correction filed on \_\_\_\_ is a) approved or b) disapproved by the Examiner.

9. Note the attached Information Disclosure Statement(s)( PTO-1449) Paper 1968

Continuation of 5. does NOT place the application in condition for allowance because: Nishida provide motivation to combine memory to multiple pixels as taught by Nishida with the light modular array as taught by Nakajima. Nishida teaches the advantages of a memory for each pixel but then teaches the advantages of providing a memory for a group of pixels. Nishida teaches, "In this case, sixteen sets of pixel component each comprised of three light emitting diodes are provided within the single display unit so that forty-eight light emitting diodes in total and forty-eight regulators in total are included there within. In such a configuration, it is still sufficient to provide single memory and single controller for the single display unit, since display information with respect to respective forty-eight light emitting diode can be stored into the single memory." Nishida, col. 13, lines 52 - 60. Thus, Nishida suggests that a single memory for a group of pixels can store individual information for each pixel.